Vidya Vikas Education Trust's iversal College of Engineering Approved by AICTE, DTE, Maharashtra State Government and Affiliated to Mumbai University

Accredited with B+ Grade by NAAC | Recognised as Linguistic (Gujarati) Minority Institution Kaman Bhiwandi Road, Survey No. 146 (Part), Village Kaman, Taluka Vasai, District Palghar - 401208, Ph: 8007478659

EXAMINATION TIME TABLE (DECEMBER 2020 / JANUARY 2021) PROGRAMME - S.E. (Electronics) (REV. -2012)(CBSGS)

SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Wednesday, December 23, 2020	12:30 p.m. to 02:30 p.m.	EXS 401	APPLIED MATHEMATICS - IV
Saturday, December 26, 2020	12:30 p.m. to 02:30 p.m.	EXC402	DISCRETE ELECTRONIC CIRCUITS
Tuesday, December 29, 2020	12:30 p.m. to 02:30 p.m.	EXC 403	MICROPROCESSOR AND PERIPHERALS
Thursday, December 31, 2020	12:30 p.m. to 02:30 p.m.	EXC404	PRINCIPLES OF CONTROL SYSTEMS
Saturday, January 2, 2021	12:30 p.m. to 02:30 p.m.	EXC405	FUNDAMENTALS OF COMMUNATION ENGINEERING
Tuesday, January 5, 2021	12:30 p.m. to 02:30 p.m.	EXC406	ELECTRICAL MACHINES



Mumbai 10th December, 2020.

Principal

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