



Vidya Vikas Education Trust's

Universal College of Engineering

Approved by AICTE, DTE, Maharashtra State Government and Affiliated to Mumbai University

DTE Code : 3460

Accredited with B+ Grade by NAAC | Recognised as Linguistic (Gujarati) Minority Institution

Kaman Bhiwandi Road, Survey No. 146 (Part), Village Kaman, Taluka Vasai, District Palghar - 401208, Ph: 8007478659

EXAMINATION TIME TABLE (JANUARY 2021)
PROGRAMME - T.E. (Electronics & Telecommunication)(REV. -2016) (Choice Based)
SEMESTER - V

Days and Dates	Time	Course Code	Paper
Thursday, January 7, 2021	3.30 p.m to 5.30 p.m	ECC501	Micropocessor & Peripherals Interfacing
Saturday, January 9, 2021	3.30 p.m to 5.30 p.m	ECC502	Digital Communication
Tuesday, January 12, 2021	3.30 p.m to 5.30 p.m	ECC503	Electromagnetic Engineering
Thursday, January 14, 2021	3.30 p.m to 5.30 p.m	ECC504	Discrete Time Signal Processing
Saturday, January 16, 2021	3.30 p.m to 5.30 p.m	ECCDLO 5011	Elective I : Microelectronics
Saturday, January 16, 2021	3.30 p.m to 5.30 p.m	ECCDLO 5012	Elective I : TV & Video Engineering
Saturday, January 16, 2021	3.30 p.m to 5.30 p.m	ECCDLO 5013	Elective I : Finite Automata Theory
Saturday, January 16, 2021	3.30 p.m to 5.30 p.m	ECCDLO 5014	Elective I : Data Compression & Encryption

Mumbai
20th December 2020



PRINCIPAL