



Vidya Vikas Education Trust's

Universal College of Engineering

Approved by AICTE, DTE, Maharashtra State Government and Affiliated to Mumbai University

DTE Code : 3460

Accredited with B+ Grade by NAAC | Recognised as Linguistic (Gujarati) Minority Institution

Kaman Bhiwandi Road, Survey No. 146 (Part), Village Kaman, Taluka Vasai, District Palghar - 401208, Ph: 8007478659

EXAMINATION TIME TABLE (JANUARY 2021)
PROGRAMME - T.E. (Electronics) (REV. -2012)(CBSGS)
SEMESTER - V

Days and Dates	Time	Course Code	Paper
Thursday, January 7, 2021	3.30 p.m to 5.30 p.m	EXC501	MICROCONTROLLERS AND APPLICATIONS
Saturday, January 9, 2021	3.30 p.m to 5.30 p.m	EXC502	DESIGN WITH LINEAR INTEGRATED CIRCUITS
Tuesday, January 12, 2021	3.30 p.m to 5.30 p.m	EXC503	ELECTROMAGNETIC ENGINEERING
Thursday, January 14, 2021	3.30 p.m to 5.30 p.m	EXC504	SIGNALS & SYSTEMS
Saturday, January 16, 2021	3.30 p.m to 5.30 p.m	EXC505	DIGITAL COMMUNICATION

Mumbai
20th December 2020



PRINCIPAL