



Vidya Vikas Education Trust's

Universal College of Engineering

Approved by AICTE, DTE, Maharashtra State Government and Affiliated to Mumbai University

DTE Code : 3460

Accredited with B+ Grade by NAAC | Recognised as Linguistic (Gujarati) Minority Institution

Kaman Bhiwandi Road, Survey No. 146 (Part), Village Kaman, Taluka Vasai, District Palghar - 401208, Ph: 8007478659

EXAMINATION TIME TABLE (DECEMBER 2020)
PROGRAMME - T.E. (Electronics & Telecommunication)(REV. -2016) (Choice Based)
SEMESTER - V

Days and Dates	Time	Course Code	Paper
Wednesday, December 2, 2020	4.30 p.m to 5.30 p.m	ECC501	Microprocessor & Peripherals Interfacing
Friday, December 4, 2020	4.30 p.m to 5.30 p.m	ECC502	Digital Communication
Monday, December 7, 2020	4.30 p.m to 5.30 p.m	ECC503	Electromagnetic Engineering
Wednesday, December 9, 2020	4.30 p.m to 5.30 p.m	ECC504	Discrete Time Signal Processing
Friday, December 11, 2020	4.30 p.m to 5.30 p.m	ECCDLO 5011	Elective I : Microelectronics
Friday, December 11, 2020	4.30 p.m to 5.30 p.m	ECCDLO 5012	Elective I : TV & Video Engineering
Friday, December 11, 2020	4.30 p.m to 5.30 p.m	ECCDLO 5013	Elective I : Finite Automata Theory
Friday, December 11, 2020	4.30 p.m to 5.30 p.m	ECCDLO 5014	Elective I : Data Compression & Encryption

10th November 2020.

Mumbai - 400 098



PRINCIPAL