University of Mumbai
Examination 2020

Program: Computer Engineering
Curriculum Scheme: Rev2016
Examination: Second Year Semester IV
Course Code: CSC403 and Course Name: Computer Organization and Architecture

Time: 1 hour   Max. Marks: 50

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For the students:- All the Questions are compulsory and carry equal marks .

Q1.	In Parallel processing, if one processor fails
Option A:	All fails
Option B:	Others continue to operate
Option C:	Complete system breakdowns
Option D:	Other nodes fail
Q2.	Flynn's classification is based on
Option A:	Instruction and data processing
Option B:	Only Instruction processing
Option C:	Only data processing
Option D:	Nor instruction neither data
Q3.	To communicate with a device, processor places _____ on address line
Option A:	device address
Option B:	processor address
Option C:	instruction address
Option D:	operand address
Q4.	The DMA Controller does not have following register
Option A:	Address register
Option B:	Word Count register
Option C:	Control register
Option D:	Flag register
Q5.	The controller that has access to a bus at an instance is known as
Option A:	Bus master
Option B:	Bus monitor
Option C:	Bus controller
Option D:	Bus initiator
Q6.	We predict the future in which page replacement algorithm?
Option A:	FIFO
Option B:	LRU
Option C:	Optimal
Option D:	Non optimal
Q7.	OS Boot Program is stored in
Option A:	ROM
Option B:	RAM

Option C:	Register
Option D:	Stack
Q8.	Following is a method to implement VM
Option A:	Paging
Option B:	Polling
Option C:	Cycle stealing
Option D:	Altering
Q9.	In Paging, Physical Memory is divided into
Option A:	Pages
Option B:	Blocks
Option C:	Frames
Option D:	Lines
Q10.	Which of the following is not a cache mapping technique
Option A:	Direct mapping
Option B:	Associative mapping
Option C:	Set associative mapping
Option D:	Relative mapping
Q11.	In SISD, data is stored in
Option A:	Shared memory
Option B:	Data not stored
Option C:	Single memory
Option D:	stack memory
Q12.	Pentium is following superscalar
Option A:	Multiple issue
Option B:	Single issue
Option C:	Two issue
Option D:	Quad issue
Q13.	Instruction Register holds _____ instruction.
Option A:	executed
Option B:	not executed
Option C:	currently being executed
Option D:	no
Q14.	In Booth's Algorithm, we perform
Option A:	Arithmetic Left Shift
Option B:	Arithmetic Right Shift
Option C:	Arithmetic Up Shift
Option D:	Arithmetic Down Shift
Q15.	'Computer Organization' refers to the operational units of the computer and their
Option A:	interconnections
Option B:	architecture

Option C:	maintenance
Option D:	storage
Q16.	Single precision representation in IEEE 754 standard occupies a
Option A:	32-bit word
Option B:	16-bit word
Option C:	64-bit word
Option D:	128-bit word
Q17.	In Von neumann architecture, instruction are stored in
Option A:	separate memory
Option B:	even memory
Option C:	odd memory
Option D:	same memory
Q18.	Move R0, R1 is example of _____ addressing mode
Option A:	register
Option B:	direct
Option C:	immediate
Option D:	indirect
Q19.	Following is not a type of hazard
Option A:	data
Option B:	structural
Option C:	control
Option D:	storage
Q20.	Which processors have variable instruction format ?
Option A:	RISC
Option B:	CISC
Option C:	both RISC and CISC
Option D:	nor RISC neither CISC
Q21.	CPI stands for
Option A:	cycles per instruction
Option B:	cost per instruction
Option C:	computing power of instruction
Option D:	computing per instruction
Q22.	The o/p of hardwired control unit is
Option A:	microinstructions
Option B:	control signals
Option C:	clock signals
Option D:	results
Q23.	Which memory is used in Micro programmed CU?
Option A:	data memory
Option B:	control memory

Option C:	stack memory
Option D:	instruction memory
Q24.	The sequence of operations carried out by Hardwired CU is determined by
Option A:	execution of logic elements
Option B:	storing of logic elements
Option C:	wiring of the logic elements
Option D:	wiring of registers
Q25.	Which technique is used widely now a days for implementing the control unit of computers
Option A:	micro programming
Option B:	hard wiring
Option C:	networking
Option D:	programming