Program: BE Electronics and Telecommunication Engineering
Curriculum Scheme: Revised 2012
Examination: Third Year Semester VI
Course Code: ETC606 and Course Name:VLSI Design

Time: 1 hour   Max. Marks: 50

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Answer Key for VLSI Design-IV

Question No.	Correct Option
1	A
2	A
3	B
4	D
5	B
6	D
7	A
8	A
9	C
10	B
11	D
12	B
13	B
14	C
15	B
16	C
17	B
18	B
19	A

20	B
21	C
22	C
23	A
24	A
25	A