EXAMINATION TIME TABLE (SEPTEMBER/OCTOBER 2020)
PROGRAMME - T.E. (Electronics & Telecommunication)(REV. -2016) (Choice Based)
SEMESTER - V

Days and Dates	Time	Course Code	Paper
25 September 2020	11.30 a.m to 12.30 p.m	ECC501	Micropocessor & Peripherals Interfacing
27 September 2020	11.30 a.m to 12.30 p.m	ECC502	Digital Communication
29 September 2020	11.30 a.m to 12.30 p.m	ECC503	Electromagnetic Engineering
29 September 2020	4.30 p.m to 5.30 p.m	ECC504	Discrete Time Signal Processing
01 October 2020	11.30 a.m to 12.30 p.m	ECCDLO 5011	Elective I : Microelectronics
01 October 2020	11.30 a.m to 12.30 p.m	ECCDLO 5012	Elective I : TV & Video Engineering
01 October 2020	11.30 a.m to 12.30 p.m	ECCDLO 5013	Elective I : Finite Automata Theory
01 October 2020	11.30 a.m to 12.30 p.m	ECCDLO 5014	Elective I : Data Compression & Encryption

PRINCIPAL

19th September 2020

Mumbai - 400 098