

Program: BE Electronics and Telecommunication Engineering

Curriculum Scheme: Revised 2016

Examination: Third Year Semester VI

Course Code: ECCDLO6021 and Course Name: Digital VLSI Design

Time: 1 hour

Max. Marks: 50

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Answer Key for Digital VLSI Design-IV

Question No.	Correct Option
1	A
2	B
3	A
4	B
5	C
6	C
7	D
8	A
9	C
10	B
11	D
12	B
13	B
14	C
15	B
16	C
17	B
18	B
19	A
20	B

21	C
22	C
23	A
24	C
25	C