



**VidyaVikas Education Trust's
Universal College of Engineering, Kaman Road, Vasai-401212
Department- Computer Engineering**

SE(A)

SECOND YEAR, DIV-A

ACADEMIC YEAR: 2017-18

CLASS ROOM NO: 520

TIME/DAY	MON	TUE	WED	THU	FRI
9:00-10:00	DLDA(SM)	AM-III(MK)	DS(LR)	OOPM(JK)	DS(LR)(L9)(B1) DLDA(VB)306(B3) OOPM(SB)(L8)(B2)
10:00-11:00	ECCF(DD)	DLDA(SM)	OOPM(JK)	ROTATING	
11:00-12:00	LIBRARY	AM-III(MK)	DIS(RG)	DLDA(SM)	DIG(RG)
12:00-12:45	LUNCH				
12:45-1:45	OOPM(SB)(L10)(B3) ECCF(DD)(408)(B1) DLDA(SM)(306)(B2)	DS(LR)	DLDA(SM)	ECCF(DD)	DS(LR)(L9)(B3) ECCF(DD)408(B2) DLDA(SM)405(B1)
1:45-2:45		DIS(RG)	AM-III(MK)	DS(LR)	
2:45-3:00	BREAK				
3:00-4:00	DS(AK)(L9)(B2) ECCF(DD)(408)(B3) OOPM(JK)(L8)(B1)	ECCF(DD)	LIBRARY	ECCF(DD)	DIS(RG)
4:00-5:00		OOPM(JK)	AM-III(MK)	AM-III(MK)	DS(LR)

SUBJECT NAME & ABBREVIATION

DS: DATA STRUCTURES
ECCF: ELECTRONIC CIRCUITS & COMMUNICATION FUNDAMENTALS
DLDA: DIGITAL LOGIC DESIGN & ANALYSIS
APPLIED MATHS-III
DIS: DISCRETE STRUCTURES
OOPM: OBJECT ORIENTED PROGRAMMING METHEDODOLOGY
DLDA(PR):
DS(PR):

FACULTY NAME

LR: Prof. Leena Raut
DD: Prof. Deepak Dhawe
SM: Prof. Snehal Modi
MK: Prof. M.Khan
RG: Prof. Rajesh Giri
JK: Prof. John Kenny

VB: Prof. Vivek
KD: Prof. Kanchan Dabre

TIMETABLE CO-ORDINATORS

PROF. JOHN KENNY/PROF. RUCHA PATHARI

HOD

PROF. HAZEL LOPEZ

PRINCIPAL

DR. J. B. PATIL



**VidyaVikas Education Trust's
Universal College of Engineering, Kaman Road, Vasai-401212
Department- Computer Engineering**

❖
SE(B)

SECOND YEAR, DIV-B

ACADEMIC YEAR: 2017-18

CLASS ROOM NO: 521

TIME/DAY	MON	TUE	WED	THU	FRI
9:00-10:00	OOPM(JK)	ROTATING	DIS(MK)	ECCF(DD)408(B2) DLDA(AP)306(B1) DS(AK)(L8)(B3)	AM-III(KT)
10:00-11:00	DS(LR)	ECCF(DD)	ECCF(DD)		DIS(MK)
11:00-12:00	DIS(MK)	OOPM(JK)	DS(LR)	ECCF(DD)	DIS(MK)
12:00-12:45	LUNCH				
12:45-1:45	LIBRARY	ECCF(DD)408(B1) DLDA(AP)306(B2) OOPM(AK)(L10)(B3)	ECCF(DD)408(B3) DS(LR)(L8)(B1) OOPM(AK)(L10)(B2)	AM-III(KT)	AM-III(KT)
1:45-2:45	AM-III(KT)			DLDA(SM)	LIBRARY
2:45-3:00	BREAK				
3:00-4:00	DS(LR)	DLDA(SM)	DLDA(SM)	DLDA(VB)(306)(B3) OOPM(AK)(L10)(B1) DS(LR)(L8)(B2)	DS(LR)
4:00-5:00	DLDA(SM)	AM-III(KT)	ECCF(DD)		OOPM(JK)

SUBJECT NAME & ABBREVIATION

DS: DATA STRUCTURES
ECCF: ELECTRONIC CIRCUITS & COMMUNICATION FUNDAMENTALS
DLDA: DIGITAL LOGIC DESIGN & ANALYSIS
APPLIED MATHS-III
DIS: DISCRETE STRUCTURES
OOPM: OBJECT ORIENTED PROGRAMMING METHODOLOGY
DLDA(PR):

FACULTY NAME

LR: Prof. Leena Raut AK:Aniket Kore
DD: Prof. Deepak Dhave
SM: Prof. Snehal Modi
MK: Prof. Kamlesh Tiwari
RG: Prof. Khan
JK: Prof. John Kenny AK:Aniket Kore
VB: Prof. Vivek

TIMETABLE CO-ORDINATORS

PROF. JOHN KENNY/PROF. RUCHA PATHARI

HOD

PROF. HAZEL LOPEZ

PRINCIPAL

DR. J. B. PATIL



VidyaVikas Education Trust's
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Department- Computer Engineering

TE(A)
THIRD YEAR, DIV-A

ACADEMIC YEAR: 218
CLASS ROOM NO: 522

TIME/DAY	MON	TUE	WED	THU	FRI
9:00-10:00	WT(VB)	SOAD(PG)	OS(HL)(L9)(B1) SOAD(CP)(L8)(B3) WT(VB)(R2)(B2)	OS(HL)(L9)(B2) BCE(522)(B1) MP(SD)R2(B3)	CN(SI)(L10)(B3) MP(AP)(R2)(B1) BCE(522)(B2)
10:00-11:00	LIBRARY	OS(HL)			
11:00-12:00	SOAD(PG)	WT(VB)	CN(SI)	MP(SD)	OS(HL)
12:00-12:45	LUNCH				
12:45-1:45	MP(SD)	BCE(AR)	MP(SD)	CN(SI)	SOAD(PG)
1:45-2:45	OS(HL)	CN(SI)	LIBRARY	BCE(AR)	ROTATING
2:45-3:00	BREAK				
3:00-4:00	CN(SI)	CN(AP)(L10)(B2) WT(VB)(R1)(B1) BCE(522)(B3)	OS(HL)(L9)(B3) CN(SI)(L10)(B1) SOAD(CP)(L7)(B2)	OS(HL)	SOAD(RC)(L8)(B1) WT(VB)(R1)(B3) MP(SD)(R2)(B2)
4:00-5:00	MP(SD)			SOAD(PG)	

SUBJECT NAME & ABBREVIATION

MP: MICROPROCESSORS
 CN: COMPUTER NETWORKS
 OS: OPERATING SYSTEMS
 SOAD: STRUCTURE AND OBJECT ORIENTED ANALYSIS AND DESIGN
 WT: WEB TECHNOLOGIES
 BCE: BUSINESS COMMUNICATION AND ETHICS
 MP(PR)
 CN(PR)

FACULTY NAME

SD: Prof. SILVIYA D'MONTE
 CR: Prof. SRIDHAR IYER
 PQR: Prof. SHILPA JAISWAL
 PG: Prof. PRANIT GAIKWAD
 VB: PROF. VIVEK BANSODE
 AR: Prof. ARCHANA RATHOD
 VS: Prof. VISHAKA SHELKE
 HL: PROF. HAZEL LOPEZ

Time Table Coordinator

Prof. John Kenny/Prof. Rucha Pathari

Head of Department

Prof. Hazel Lopez

Principal

Dr. J. B. Patil



**VidyaVikas Education Trust's
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Department- Computer Engineering**

TE(B)

THIRD YEAR, DIV-B

ACADEMIC YEAR: 2017-18

CLASS ROOM NO: 523

TIME/DAY	MON	TUE	WED	THU	FRI
9:00-10:00	SOAD(PG)	WT(VS)	CN(PC)(L10)(B2) SOAD(RC)(L8)(B3) WT(VS)R1(B1)	MP(PN)(L7)(B1) WT(HP)(R1)(B2) BCE(523)(B3)	BCE(523)(B1) OS(MS)(L7)(B2) MP(HP)(R2)(B3)
10:00-11:00	BCE(TS)	MP(SD)			
11:00-12:00	BCE(TS)	CN(CR)	SOAD(PG)	SOAD(PG)	MP(SD)
12:00-12:45	LUNCH				
12:45-1:45	WT(VB)	SOAD(PS)(L8)(B1) BCE(523)(B2) WT(VS)(R1)(B3)	OS(SJ)	BCE(TS)	OS(SJ)(L7)(B3) CN(KD)(L10)(B1) MP(PS)(R2)(B2)
1:45-2:45	CN(CR)		ROTATING	OS(SJ)	
2:45-3:00	BREAK				
3:00-4:00	CN(CR)	SOAD(PG)	MP(SD)	BCE(TS)	OS(SJ)(L9)(B1) CN(AP)(L8)(B3) SOAD(PS)(L7)(B2)
4:00-5:00	OS(SJ)	OS(SJ)	CN(CR)	MP(SD)	

SUBJECT NAME & ABBREVIATION

MP: MICROPROCESSORS
CN: COMPUTER NETWORKS
OS: OPERATING SYSTEMS
SOAD: STRUCTURE AND OBJECT ORIENTED ANALYSIS AND DESIGN
WT: WEB TECHNOLOGIES
BCE: BUSINESS, COMMUNICATION AND ETHICS
CN(PR)
MP(PR)
HL(PR)

FACULTY NAME

SD: Prof. SILVIYA D'MONTE
CR: Prof. CHINMAY RAUT
SJ: Prof. SHILPA JAISWAL
PG: Prof. PRANIT GAIKWAD
VB: Prof. VIVEK Solavande
TS: Prof. TRIPTI SAUN
KD: Prof. KANCHAN DABRE
VS: Prof. VISHAKA SHELKE
HL: Prof. HAZEL LOPEZ

Time Table Coordinator

Prof. John Kenny/Prof. Rucha Pathari

Head of Department

Prof. Hazel Lopez

Principal

Dr. J. B. Patil



VidyaVikas Education Trust's
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Department- Computer Engineering

BE(A)

FINAL YEAR, DIV-A

ACADEMIC YEAR: 2017-18

CLASS ROOM NO: 524

TIME/DAY	MON	TUE	WED	THU	FRI
9:00-10:00	IP(PC)(L10)(B1) SA(MS)(L9)(B1) CSS(SI)(L8)(B3) NTAL(RP)(L7)(B2)	IP(KD)(L10)(B2) SA(MS)(L9)(B2) CSS(PN)(L8)(B1) DSP(RC)(R2)(B3)	IP(SA)(KD)(SJ)	CSS(SI)	CAREER LAUNCHED
10:00-11:00			NTAL(RP)	NTAL(RP)	
11:00-12:00	DSP(SP)	DSP(SP)	AI(VS)	DSP(SP)	
12:00-12:45	LUNCH				
12:45-1:45	AI(VS)	CSS(SI)	DSP(SP)	NTAL(RP)(L7)(B1) AI(VS)(R1)(B3) DSP(PC)(R2)(B2)	CAREER LAUNCHED
1:45-2:45	IP(SA)(KD)(SJ)	IP(SA)(KD)(SJ)	CSS(SI)		
2:45-3:00	BREAK				
3:00-4:00	AI(VS)	NTAL(RP)(L7)(B3) AI(VS)(L8)(B2) DSP(PC)(R2)(B1)	IP(SA)(KD)(SJ)	IP(KD)(L10)(B3) SA(MS)(L9)(B3) AI(PC)(R2)(B1) CSS(PN)(L8)(B2)	PROJECT
4:00-5:00	CSS(SI)		AI(VS)		

SUBJECT NAME & ABBREVIATION

IP:IMAGE PROCESSING
 SA:SOFTWARE ARCHITECTURE
 CSS:CRYPTOGRAPHY AND SYSTEM SECURITY
 IYER
 AI:ARTIFICIAL INTELLIGENCE
 DSP:DIGITAL SIGNAL PROCESSING
 NTAL:NETWORK THREATS AND ATTACKS LABORATORY

FACULTY NAME

KD:PROF.KANCHAN DABRE
 SJ:PROF.SHILPA JAISWAL
 SI:JPROF.SRIDHAR
 VS:PROF.VISHAKA SHELKE
 SP:PROF.SHARVARI PATIL
 RP:PROF.RUCHA PATHARI

TIMETABLE CO-ORDINATORS

PROF.JOHN KENNY/PROF.RUCHA PATHARI

HOD

PROF.HAZEL LOPEZ

PRINCIPAL

DR.J.B PATIL



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Department- Computer Engineering

BE(B)

FINAL YEAR, DIV-B

ACADEMIC YEAR: 2017-18

CLASS ROOM NO: 525

TIME/DAY	MON	TUE	WED	THU	FRI
9:00-10:00	IP(KD)(L10)(B1) SA(MS)(L9)(B1) CSS(PN)(R1)(B2) DSP(SB)(R2)(B3)	IP(KD)(L10)(B2) SA(MS)(L9)(B2) DSP(SB)(R1)(B1) NTAL(CP)(L7)(B3)	IP/SA(KD/SJ)	DSP(SP)	CAREER LAUNCHER
10:00-11:00			DSP(SP)	CSS(CR)	
11:00-12:00	CSS(CR)	NTAL(RP)	AI(RC)	AI(RC)	
12:00-12:45	LUNCH				
12:45-1:45	DSP(SP)	NTAL(RP)	NTAL(CP)(L7)(B2) CSS(PN)(R2)(B3) AI(RC)(R1)(B1)	CSS(CR)	CAREER LAUNCHER
1:45-2:45	IP/SA(KD/SJ)	IP/SA(KD/SJ)		AI(RC)	
2:45-3:00	BREAK				
3:00-4:00	NTAL(SB)(L7)(B1) DSP(SP)(R2)(B2) AI(RC)(R1)(B3)	AI(RC)	IP/SA(KD/SJ)	IP(KD)(L10)(B3) SA(MS)(L9)(B3) AI(RC)R1(B2) CSS(CR)(L7)(B1)	PROJECT
4:00-5:00		CSS(CR)	DSP(SP)		

SUBJECT NAME & ABBREVIATION

IP:IMAGE PROCESSING
 SA:SOFTWARE ARCHITECTURE
 CSS:CRYPTOGRAPHY AND SYSTEM SECURITY
 CR:JPROF.CHINMAY RAUT
 AI:ARTIFICIAL INTELLIGENCE
 DSP:DIGITAL SIGNAL PROCESSING
 NTAL:NETWORK THREATS AND ATTACKS LABORATORY
 DSP:DIGITAL SIGNAL PROCESSING(PR)

FACULTY NAME

KD:PROF.KANCHAN DABRE
 SJ:PROF.SILPHA JAISWAL
 RC:PROF.RAJESH CHAPKE
 SP:PROF.SHARVARI PATIL
 RP:PROF.RUCHA PATHARI
 RC:PROF.RAJESH

TIMETABLE CO-ORDINATORS

PROF.JOHN KENNY/PROF.RUCHA PATHARI

HOD

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DR.J.B PATIL